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	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CYPR-CD01207M	

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11	Walters; "Computer-Aided Prototyping For ASIC-Based Systems"; 1991; IEEE Design and Test of Computers	<input type="checkbox"/>
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15	Xerox; "Mesa Debugger Documentation"; April 1979; Xerox Systems Development Department; Version 5.0; pages 1-30	<input type="checkbox"/>
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17	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; pages 1-5	<input type="checkbox"/>
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22	"In-Circuit Emulators- descriptions of the major ICEs around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm	<input type="checkbox"/>
23	CYPR-CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation"; October 10, 2001; 09/975,104; Snyder	<input type="checkbox"/>
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